

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tsung-Han Chiang

**Serial No. :**

09/872,246

**Group Art Unit :**

2666

**Filed :**

May 31, 2001

**Examiner :**

Patel, Jay P.

**For :**

Dynamic Break Loop for Closed  
Loop Unmanaged Stacking  
Switches

**Atty Docket :**

/ 00-483

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

12/14/05

Date

Connie Del Castillo

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

**Official Draftsman**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
408-433-7475

Respectfully submitted,

Henry Groth

Henry Groth

Reg. No. 39,696

Date: 12/14/2005